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09/071875

CofC

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Blandy**Serial No.: **09/671,875**Filed: **September 28, 2000****For: Apparatus and Method for Creating
Instruction Bundles in an Explicitly
Parallel Architecture**

§

§ Group Art Unit: **2183**

§

§ Examiner: **Tsai, Henry**

§

§ Attorney Docket No.: **AUS9-2000-0587-US1**

§

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By: Dell Whitton

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TRANSMITTAL DOCUMENT

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Alexandria, VA 22313-1450

Certificate
JUN 02 2006

of Correction

Sir:

ENCLOSED HEREWITH:

- Request for Certification of Correction of Office Mistake;
- Certificate of Correction; and
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No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to IBM Corporation Deposit Account No. 09-0447.

Respectfully submitted,

Duke W. Yee

Duke W. Yee

Registration No. 34,285

YEE & ASSOCIATES, P.C.

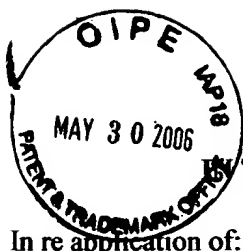
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(972) 385-8777

ATTORNEY FOR APPLICANT

JUN 05 2006



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: **Blandy**

Serial No.: **09/671,875**

Filed: **September 28, 2000**

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By: *Dell Whitton*

Dell Whitton

REQUEST FOR CERTIFICATE OF CORRECTION OF OFFICE MISTAKE

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

We would like to request a Certificate of Correction of Office Mistake with regard to issued patent 6,912,647 B1.

There is an error with respect to the following data:

Col. 10, line 7: after "group" insert

—, wherein creating one or more instruction bundles is performed in view of one or more of the following rules:

- 1) instructions of the same instruction type will preserve their original order;
- 2) branches will normally appear only in the final bundle of an instruction group;
- 3) for architectures where a number of M execution units is equal or less than a number of concurrent bundles, MM templates will only be used when there are three or fewer instructions remaining in the group;
- 4) instructions are taken in order of their flexibility in terms of where that instruction can be placed in the available bundle types; and
- 5) MBB and BBB templates are avoided when only a single B instruction remains.—

JUN 05 2006

Col. 10, line 65: after "group" delete

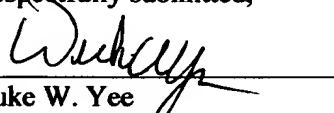
“; and

means for dynamically creating one or more instruction bundles based on the number of each possible type of instruction in the one or more instructions of the instruction group”.

Col. 12, line 37: after “claim” delete “25” and insert –27–.

No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to IBM Corporation Deposit Account No. 09-0447.

Respectfully submitted,



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ATTORNEY FOR APPLICANT

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,912,647 B1

DATED : Jun. 28, 2005

INVENTOR(S) : Blandy

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Col. 10, line 7: after "group" insert

—, wherein creating one or more instruction bundles is performed in view of one or more of the following rules:

- 1) instructions of the same instruction type will preserve there original order;
- 2) branches will normally appear only in the final bundle of an instruction group;
- 3) for architectures where a number of M execution units is equal or less than a number of concurrent bundles, MM templates will only be used when there are three or fewer instructions remaining in the group;
- 4) instructions are taken in order of their flexibility in terms of where that instruction can be placed in the available bundle types; and
- 5) MBB and BBB templates are avoided when only a single B instruction remains.—

Col. 10, line 65: after "group" delete

"; and

means for dynamically creating one or more instruction bundles based on the number of each possible type of instruction in the one or more instructions of the instruction group".

Col. 12, line 37: after "claim" delete "25" and insert —27—.

MAILING ADDRESS OF SENDER:

IBM Corporation, IP Law Dept.
11400 Burnet Road
Austin, TX 78758

PATENT NO: 6,912,647 B1

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